

# CIC-56TS-40D-A6-YAM-S (REV 2)

56-Lead TSOP to a 40-Pin DIP

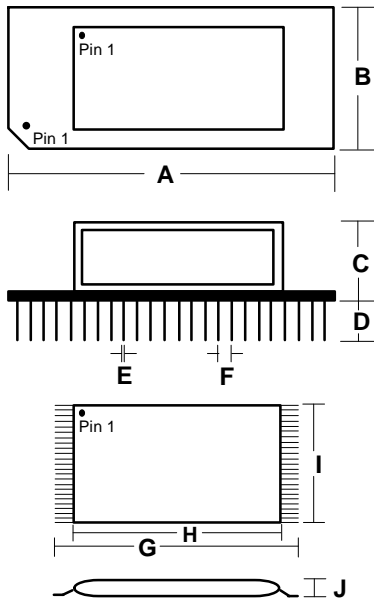
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<b>TSOP DEVICES:</b>	(EXAMPLES)
E28F200BL / BV / BX	
E28F400BL / BV / BX	

<b>SOCKET:</b> Yamaichi IC191-0562-003N
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<u>PRICING:</u>	<u>QTY</u>	<u>PRICE EACH</u>
	1-4	\$190.00
	5-9	\$171.00
	10-24	\$161.50
	25-49	\$142.50

**TYPICAL DIMENSIONS:** (Drawings not to scale)



DIMENSION	MM	INCH
A	62.23	2.450
B	25.40	1.000
C	22.86	0.900
D Pin Length	10.16	0.400
E Pin Diameter	0.64	0.025
F Pin Spacing	2.54	0.100
ROW SPACING	15.24	0.600
G	20.00	0.787
H	18.40	0.724
I	14.00	0.551
J	1.20	0.047
PITCH	0.50	0.020

**PIN CONFIGURATION:**

BASE 40-Pin DIP	PIN NAMES	SOCKET 56-Lead TSOP
1	RP#, V <sub>PP</sub> <sup>2</sup>	14, 17
2	A <sub>17</sub>	20
3	DQ <sub>15</sub> / A-1	52
4	DQ <sub>14</sub>	50
5	DQ <sub>13</sub>	48
6	DQ <sub>12</sub>	46
7	DQ <sub>11</sub>	42
8	DQ <sub>10</sub>	40
9	DQ <sub>9</sub>	38
10	DQ <sub>8</sub>	36
11	GND (Planes 2 & 5)	33
12	DQ <sub>7</sub>	51
13	DQ <sub>6</sub>	49
14	DQ <sub>5</sub>	47
15	DQ <sub>4</sub>	45
16	DQ <sub>3</sub>	41
17	DQ <sub>2</sub>	39
18	DQ <sub>1</sub>	37
19	DQ <sub>0</sub>	35
20	OE#	34
21	A <sub>0</sub>	31
22	A <sub>1</sub>	27
23	A <sub>2</sub>	26
24	A <sub>3</sub>	25
25	A <sub>4</sub>	24
26	A <sub>5</sub>	23
27	A <sub>6</sub>	22
28	A <sub>7</sub>	21
29	A <sub>8</sub>	10
30	GND (Planes 2 & 5)	53
31	A <sub>9</sub>	9
32	A <sub>10</sub>	8
33	A <sub>11</sub>	7
34	A <sub>12</sub>	6
35	A <sub>13</sub>	5
36	A <sub>14</sub>	4
37	A <sub>15</sub>	3
38	A <sub>16</sub>	55
39	WE#	13
40	V <sub>CC</sub> (Planes 3 & 4)	43,44
V <sub>CC</sub> or GND Planes	BYTE# <sup>1</sup>	54
V <sub>CC</sub> Planes	WP# / DU	18
GND Planes	CE#	32

**NOTES:**

1. BYTE# (pin 54) can be jumpered to GND (x8) or V<sub>CC</sub> (x16) planes of PC board.
2. RP# (pin 14) is tied to V<sub>PP</sub>.
  - Flexible x8 or x16 adapter.
  - NC (pins 1,2,11,12,15,16,28,29,30 & 56) and NC/ A<sub>18</sub> (pin 19) are not connected.
  - Bypass capacitors (Type 1206) may be added between V<sub>CC</sub> planes & GND planes; as required by Programmer Vendor

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